IN THE SPECIFICATION

Page 1, between lines 10 and 11, please insert the heading: FIELD OF THE INVENTION

Page 1, between lines 15 and 16, please insert the heading:
DISCUSSION OF THE BACKGROUND

Please replace the paragraph at page 2, lines 9-14, with the following rewritten paragraph:

The display-data RAM 50 is provided with a cell array 51 on which RAM cells 52 are arranged in a matrix, an address decoder 55, a display-data read counter/decoder 57, an I/F (interface) controller 60, a data I/O circuit 62 and an oscillator 65. Each RAM cell 52 consists of two transistors, a latch circuit having two inverter gates, and a three-state driver.

Please replace the paragraph at page 3, lines 11-16, with the following rewritten paragraph:

The known device shown in FIG. 7 is provided with a display-data RAM 50A of the same type as the display-data RAM 50 (FIG. 6) except that the RAM 50A has two transistors and two inverter gates for each RAM cell 53. The display-data RAM 50A is therefore a single-port RAM 50A. The known device in FIG. 7 is also provided with a segment-electrode driver 46.

Please replace the paragraph at page 6, lines 14-20, with the following rewritten paragraph:

The display-data RAM 50B is provided with a cell array 51A on which RAM cells 53 are arranged in a matrix, an address decoder 55, an inverter gate 56, a display-data read counter/decoder 57, an I/F (interface) controller 60, a data I/O circuit 62 and an oscillator 65. The display-data RAM 50B is a single-port RAM in which each RAM cell 53 consists of two transistors and a latch circuit having two inverter gates.

Please replace the paragraph at page 8, lines 11-16, with the following rewritten paragraph:

The output of the latch circuit 14 is further stored in the latch circuit 16 in response to an inverted signal of a latch signal S_{L1} from the display-data read counter/decoder 57. The output of the latch circuit 16 is sent to the corresponding signal line via the driver 18 and displayed on the liquid crystal displaying section 2.